

AMENDMENTS TO THE CLAIMS

1. (CURRENTLY AMENDED) A method for decoding a bitstream comprising the steps of:

(A) receiving a first encoded bitstream, wherein said first encoded bitstream comprises an intra-only frame picture encoded bitstream comprising a frame header and alternating macroblock rows, with each row containing data for a plurality of vertical lines from a single respective field;

(B) generating a first field ~~picture~~ header and a second field ~~picture~~ header in response to said frame header of said first encoded bitstream, wherein said first field ~~picture~~ header comprises a copy of said frame header modified to signal a first field picture and said second field header comprises a copy of said frame header modified to signal a second field picture;

(C) storing said first field header and macroblock rows containing the data for the plurality of vertical lines from a first field of the frame picture in a first buffer and storing said second field ~~picture~~ comprises header and macroblock rows containing the data for the plurality of vertical lines from a second field of the frame picture in a second buffer; and

(E D) generating a second encoded bitstream comprising (i) said first field ~~picture~~ and header, (ii) said macroblock rows containing the data for the plurality of vertical

25 lines from said first field of the frame picture, (iii) said second
field ~~picture~~ header and (iv) said macroblock rows containing the
data for the plurality of vertical lines from said second field of
the frame picture, wherein said second encoded bitstream comprises
an intra-only field picture encoded bitstream and is decodable as
interlaced field pictures using an MPEG-2 compliant decoder.

2. (CURRENTLY AMENDED) The method according to claim 1,
wherein said generating steps further comprise:

5 copying ~~a~~ said frame header from said first bitstream
into a first field header portion of ~~a~~ said first ~~field~~ buffer and
a second field header portion of ~~a~~ said second ~~field~~ buffer; and

modifying (i) a portion of said first field header
portion to indicate a top field picture and (ii) a portion of said
second field header portion to indicate a bottom field picture.

3. (CURRENTLY AMENDED) The method according to claim 2,
wherein said generating steps further comprise:

5 copying a plurality of said macroblock rows from said
first bitstream to said first ~~field~~ buffer and said second ~~field~~
buffer, wherein said copying alternates between said first and said
second buffers after each macroblock row.

4. (CURRENTLY AMENDED) The method according to claim 3, wherein said generating steps further comprise:

adjusting a slice number of each macroblock row in said first ~~field~~ buffer and said second ~~field~~ buffer to increment consecutively.

5. (CURRENTLY AMENDED) The method according to claim 1, wherein step (E D) further comprises:

writing (i) said first field ~~picture~~ and header, (ii) said macroblock rows containing the data for the plurality of vertical lines from said first field of the frame picture, (iii) said second field ~~picture~~ header and (iv) said macroblock rows containing the data for the plurality of vertical lines from said second field of the frame picture consecutively to said second encoded bitstream.

6. (CURRENTLY AMENDED) The method according to claim 4, wherein step (E D) comprises:

writing said first ~~field~~ buffer followed by said second ~~field~~ buffer to said second encoded bitstream.

7. (CURRENTLY AMENDED) The method according to claim 1, further comprising the step of:

presenting said second encoded bitstream to a video decoder.

8. (ORIGINAL) The method according to claim 7, wherein said video decoder is configured to support a field picture mode.

9. (CURRENTLY AMENDED) The method according to claim 7, further comprising:

decoding said second encoded bitstream into a first decoded field picture and a second decoded field picture; and

5 presenting even field lines on a television monitor in response to said first decoded field picture ~~of said second bitstream~~ and odd field lines on said television in response to said second decoded field picture ~~of said second bitstream~~.

10. (CURRENTLY AMENDED) An apparatus comprising:

5 means for receiving a first encoded bitstream, wherein said first encoded bitstream comprises an intra-only frame picture encoded bitstream comprising a frame header and alternating macroblock rows, with each row containing data for a plurality of vertical lines from a single respective field;

 means for generating a first field ~~picture~~ header and a second field ~~picture~~ header in response to said frame header of said first encoded bitstream, wherein said first field ~~picture~~

10 header comprises a copy of said frame header modified to signal a
first field picture and said second field header comprises a copy
of said frame header modified to signal a second field picture;

means for storing said first field header and macroblock
rows containing the data for the plurality of vertical lines from
15 a first field of the frame picture in a first buffer and storing
said second field ~~picture comprises~~ header and macroblock rows
containing the data for the plurality of vertical lines from a
second field of the frame picture in a second buffer; and

means for generating a second encoded bitstream
20 comprising (i) said first field ~~picture and~~ header, (ii) said
macroblock rows containing the data for the plurality of vertical
lines from said first field of the frame picture, (iii) said second
field ~~picture~~ header and (iv) said macroblock rows containing the
data for the plurality of vertical lines from said second field of
25 the frame picture, wherein said second encoded bitstream comprises
an intra-only field picture encoded bitstream and is decodable as
interlaced field pictures using an MPEG-2 compliant decoder.

11. (CURRENTLY AMENDED) An apparatus comprising:

a circuit configured to

(i) receive a first encoded bitstream, wherein said
first encoded bitstream comprises an intra-only frame picture
5 encoded bitstream comprising a frame header and alternating

macroblock rows, with each row containing data for a plurality of vertical lines from a single respective field,

(ii) generate a first field ~~picture~~ header and a second field ~~picture~~ header in response to said frame header of
10 said first ~~encoded~~ bitstream, wherein said first field ~~picture~~ header comprises a copy of said frame header modified to signal a
first field picture and said second field header comprises a copy
of said frame header modified to signal a second field picture;

(iii) store said first field header and macroblock
15 rows containing the data for the plurality of vertical lines from
a first field of the frame picture in a first buffer and storing
said second field ~~picture~~ ~~comprises~~ header and macroblock rows
containing the data for the plurality of vertical lines from a
second field of the frame picture in a second buffer, and

(~~iii~~ iv) generate a second encoded bitstream
20 comprising (i) said first field ~~picture~~ and header, (ii) said
macroblock rows containing the data for the plurality of vertical
lines from said first field of the frame picture, (iii) said second
field ~~picture~~ header and (iv) said macroblock rows containing the
25 data for the plurality of vertical lines from said second field of
the frame picture, wherein said second encoded bitstream comprises
an intra-only field picture encoded bitstream and is decodable as
interlaced field pictures using an MPEG-2 compliant decoder.

12. (CURRENTLY AMENDED) The apparatus according to claim 11, wherein said circuit comprises:

a first field buffer;

a second field buffer; and

5 a transform circuit configured to (i) copy a said frame header from said first encoded bitstream into a first field header portion of said first field buffer and a second field header portion of said second field buffer.

13. (ORIGINAL) The apparatus according to claim 12, wherein said transform circuit is further configured to:

modify (i) a portion of said first field header portion to indicate a top field picture and (ii) a portion of said second
5 field header portion to indicate a bottom field picture.

14. (CURRENTLY AMENDED) The apparatus according to claim 12, wherein said transform circuit is further configured to:

copy a plurality of said macroblock rows from said first encoded bitstream to said first field buffer and said second field
5 buffer, wherein said copying alternates between said first and said second field buffers after each macroblock row.

15. (PREVIOUSLY PRESENTED) The apparatus according to claim 14, wherein said transform circuit is further configured to:

adjust a slice number of each macroblock row in said first field buffer and said second field buffer to increment consecutively.

16. (CURRENTLY AMENDED) The apparatus according to claim 12, wherein said transform circuit is further configured to:

write ~~an output from~~ the contents of said first field buffer and ~~an output from~~ the contents of said second field buffer consecutively to said second encoded bitstream.

17. (CURRENTLY AMENDED) The apparatus according to claim 11, further comprising:

a video decoder circuit configured to receive said second encoded bitstream.

18. (ORIGINAL) The apparatus according to claim 17, wherein said video decoder circuit is further configured to support a field picture mode.

19. (CURRENTLY AMENDED) The apparatus according to claim 17, wherein said video decoder circuit is further configured to:

decode said second encoded bitstream into a first decoded field picture and a second decoded field picture; and

present even field lines on a television monitor in response to said first decoded field picture ~~of said second bitstream~~ and odd field lines on said television in response to said second decoded field picture ~~of said second bitstream~~.

20. (CURRENTLY AMENDED) The apparatus according to claim 11, wherein said first encoded bitstream comprises an intra-only MPEG-2 frame picture stream.

21. (CURRENTLY AMENDED) The apparatus according to claim 16, wherein said transform circuit is further configured to:

write sequence-related information from said first encoded bitstream directly to said second encoded bitstream.

22. (CURRENTLY AMENDED) The apparatus according to claim 21, wherein said transform circuit modifies one or more portions of sequence-related headers from said first encoded bitstream prior to output in said second encoded bitstream.